

|     |        |                                       |
|-----|--------|---------------------------------------|
| BRS | 503111 | controller or (control adj processor) |
| BRS | 2432   | fast adj2 clock                       |
| BRS | 3880   | (high adj speed) adj2 clock           |
| BRS | 6043   | S94 or S95                            |
| BRS | 1772   | slow adj2 clock                       |
| BRS | 715    | (low adj speed) adj2 clock            |
| BRS | 2396   | S97 or S98                            |
| BRS | 1265   | S96 and S99                           |
| BRS | 580    | S93 and S100                          |
| BRS | 594013 | input\$4 near3 (signal data)          |
| BRS | 683756 | output\$4 near3 (signal data)         |
| BRS | 470    | S101 and S102 and S103                |
| BRS | 123009 | (one single) adj3 cycle               |
| BRS | 76247  | (one single) adj3 period              |
| BRS | 186134 | S105 or S106                          |
| BRS | 268    | S104 and S107                         |
| BRS | 2374   | 713/400-501.ccls.                     |
| BRS | 1049   | 713/503.ccls. 713/600.ccls.           |
| BRS | 2967   | S109 or S110                          |
| BRS | 48     | S108 and S111                         |

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